



1 / 10

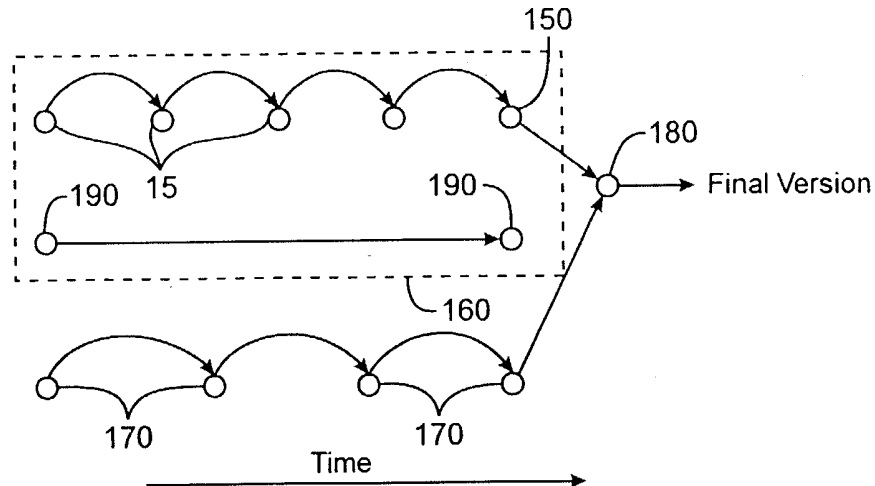


FIG. 1

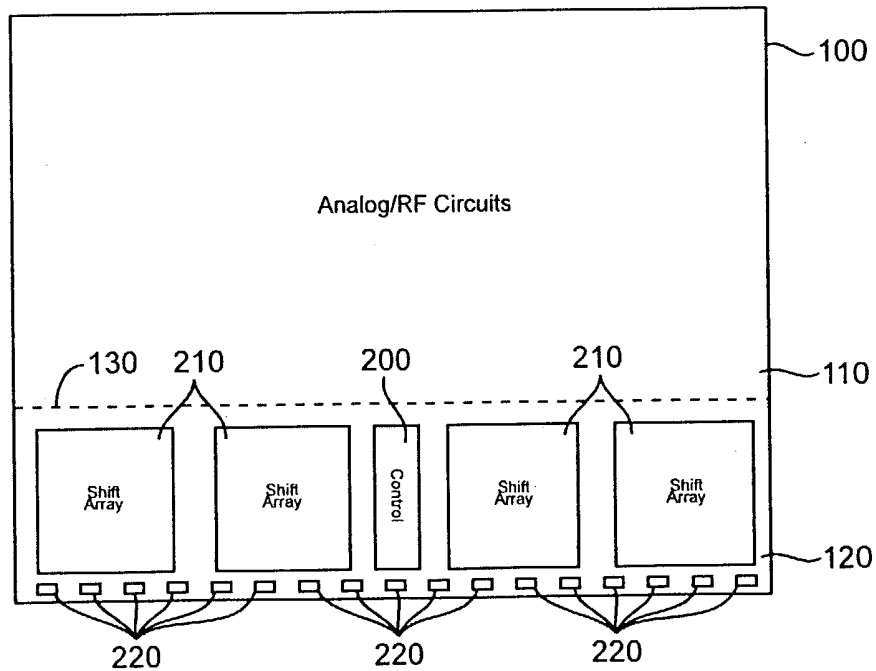


FIG. 2

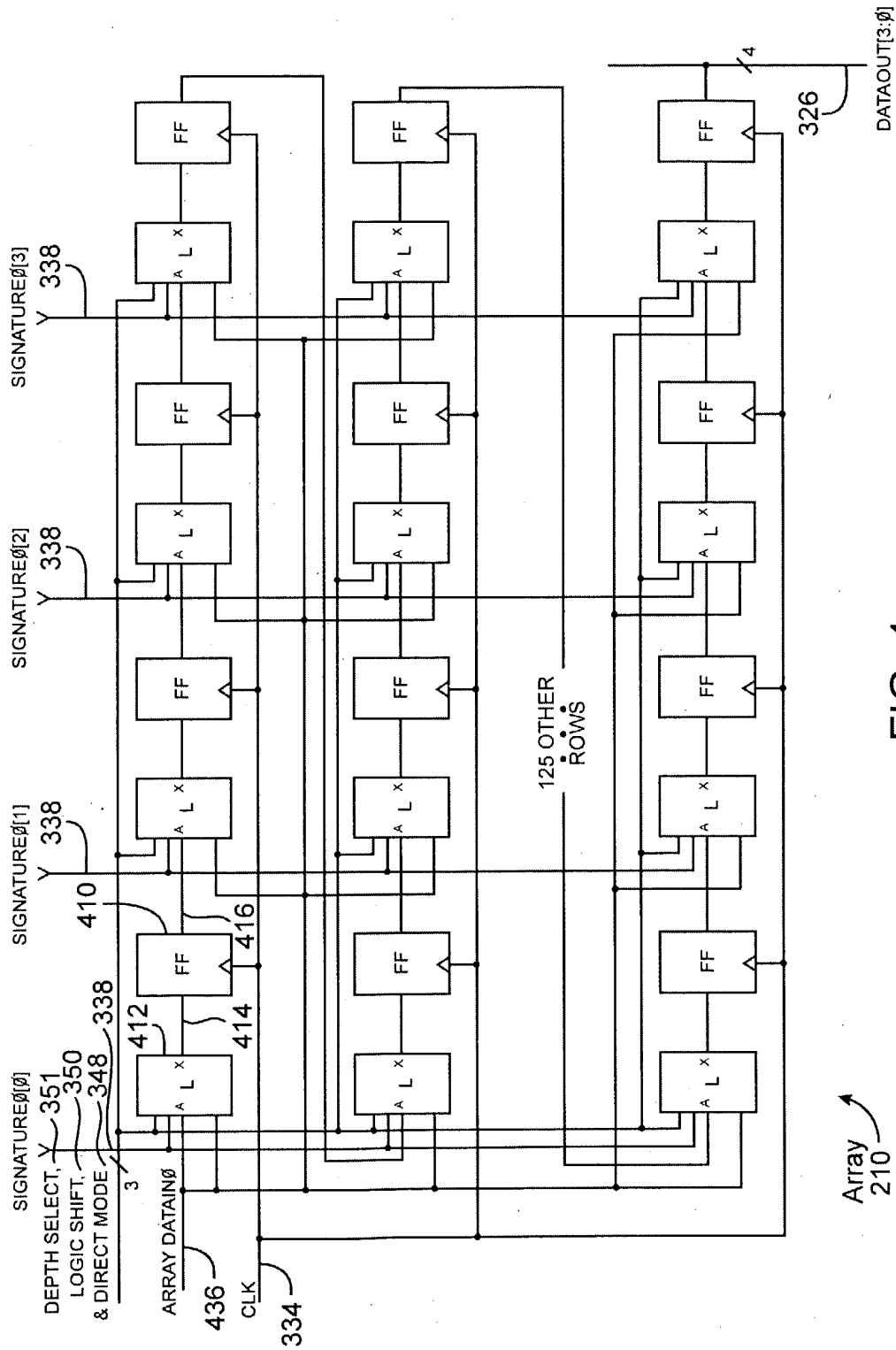


FIG. 4

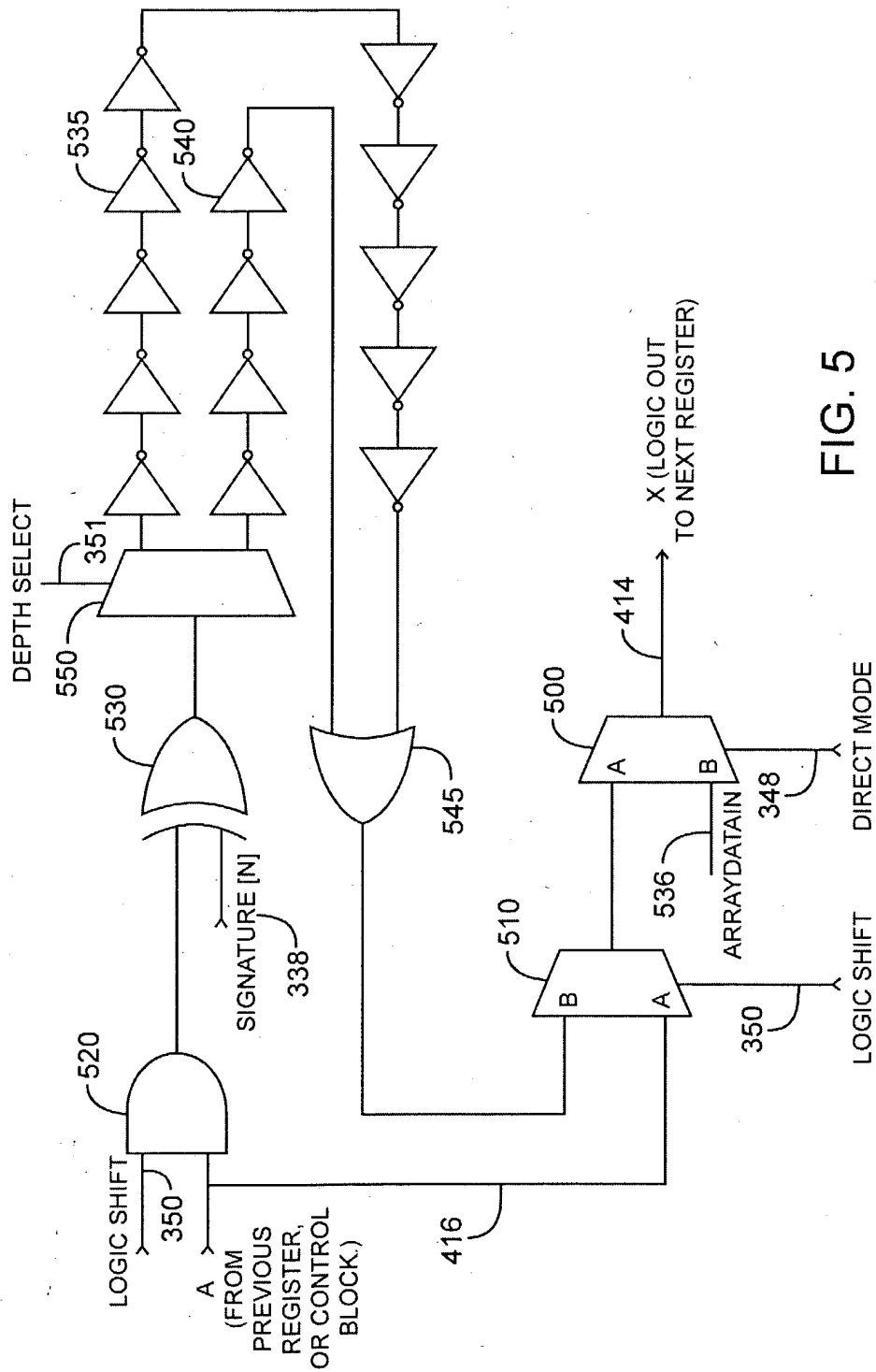


FIG. 5



5 / 10

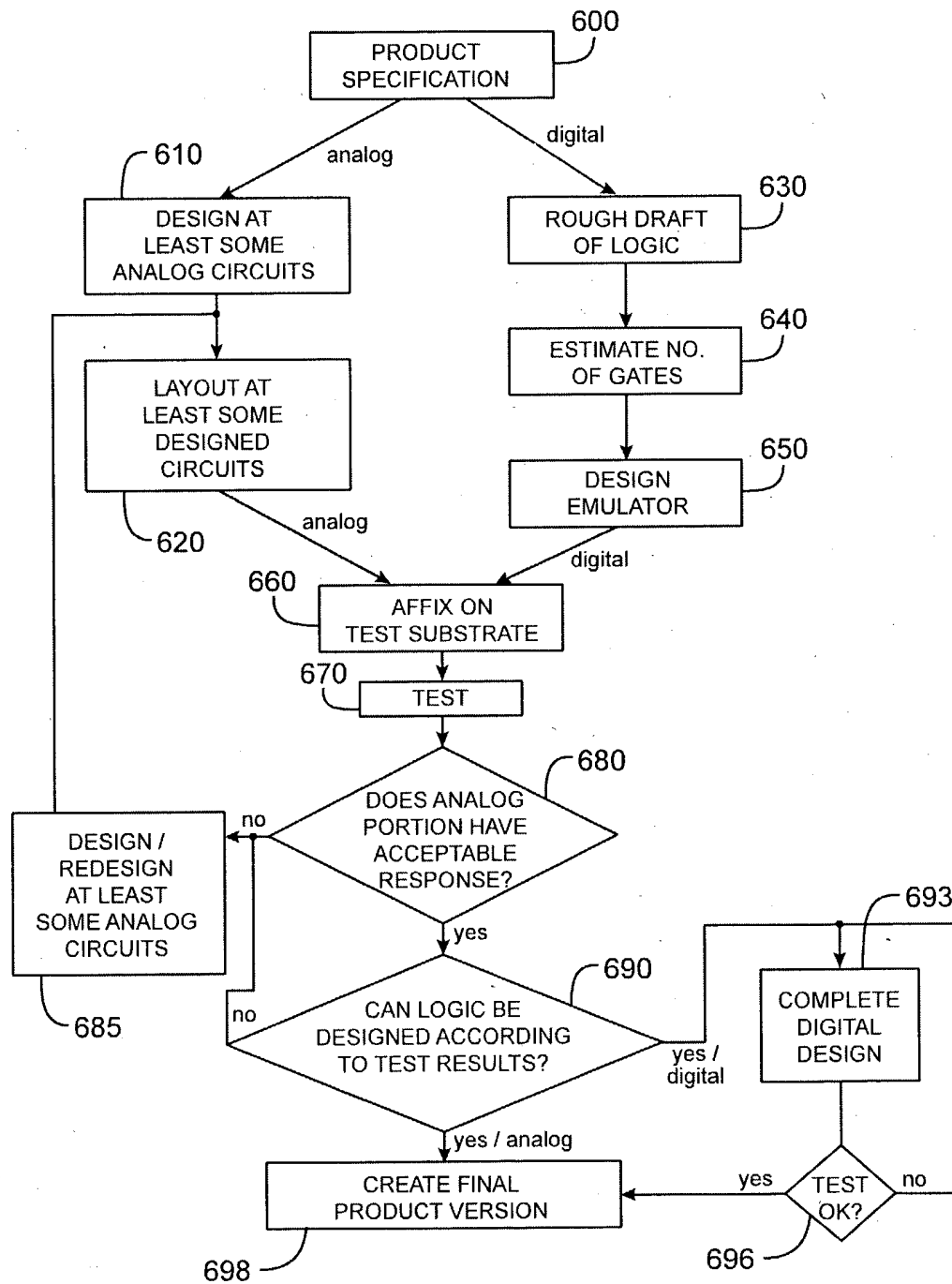


FIG. 6



6 / 10

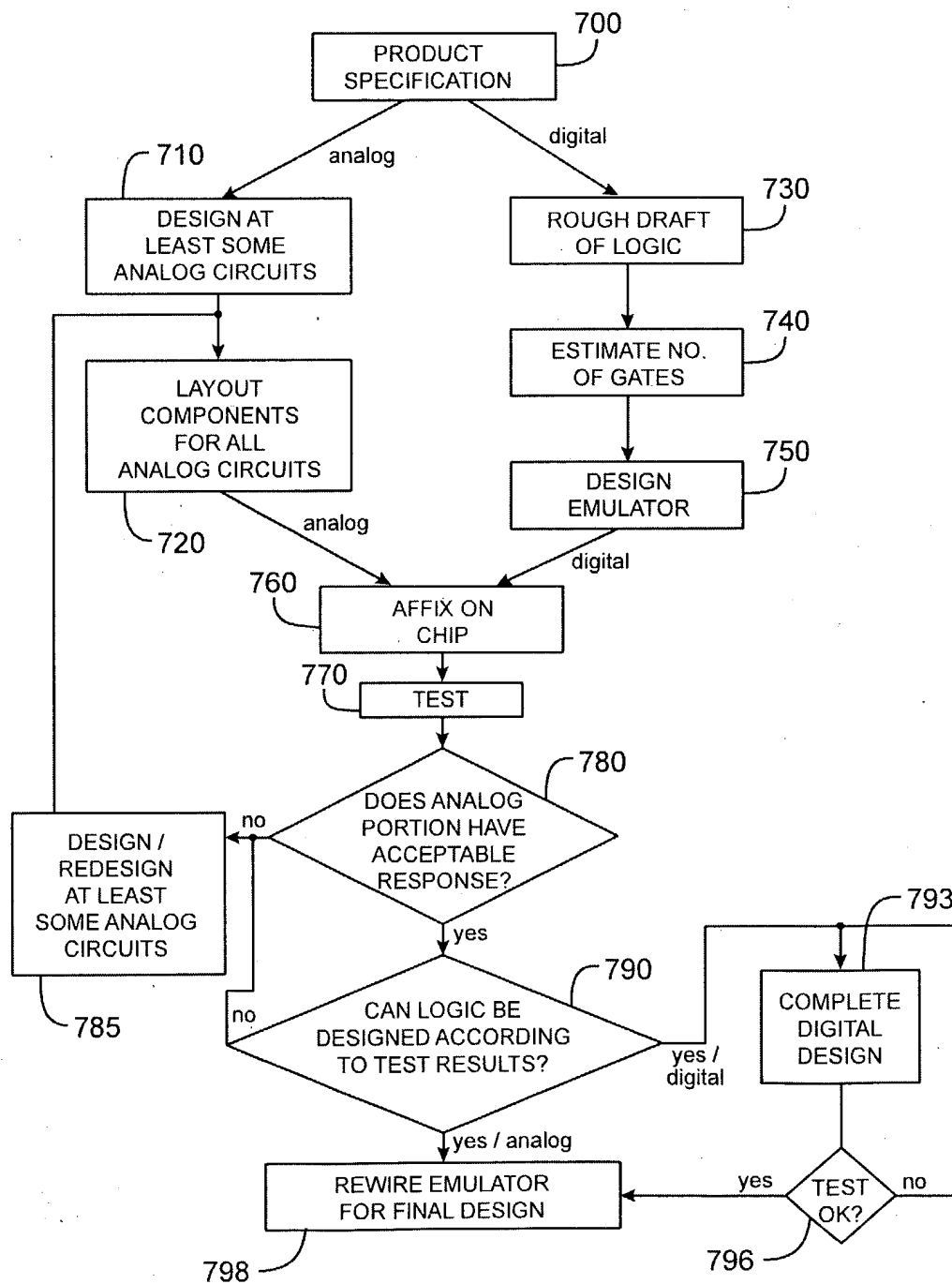
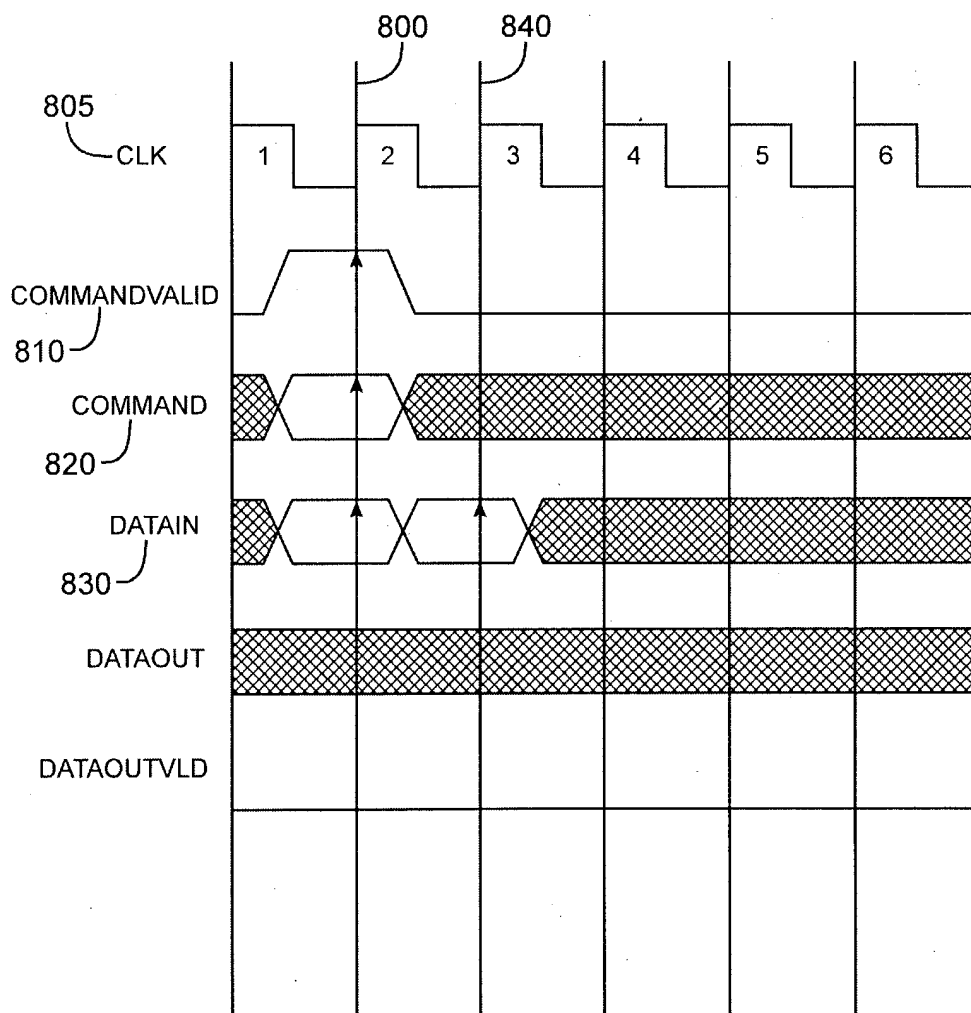


FIG. 7



7 / 10



Configuration Register Write

FIG. 8



8 / 10

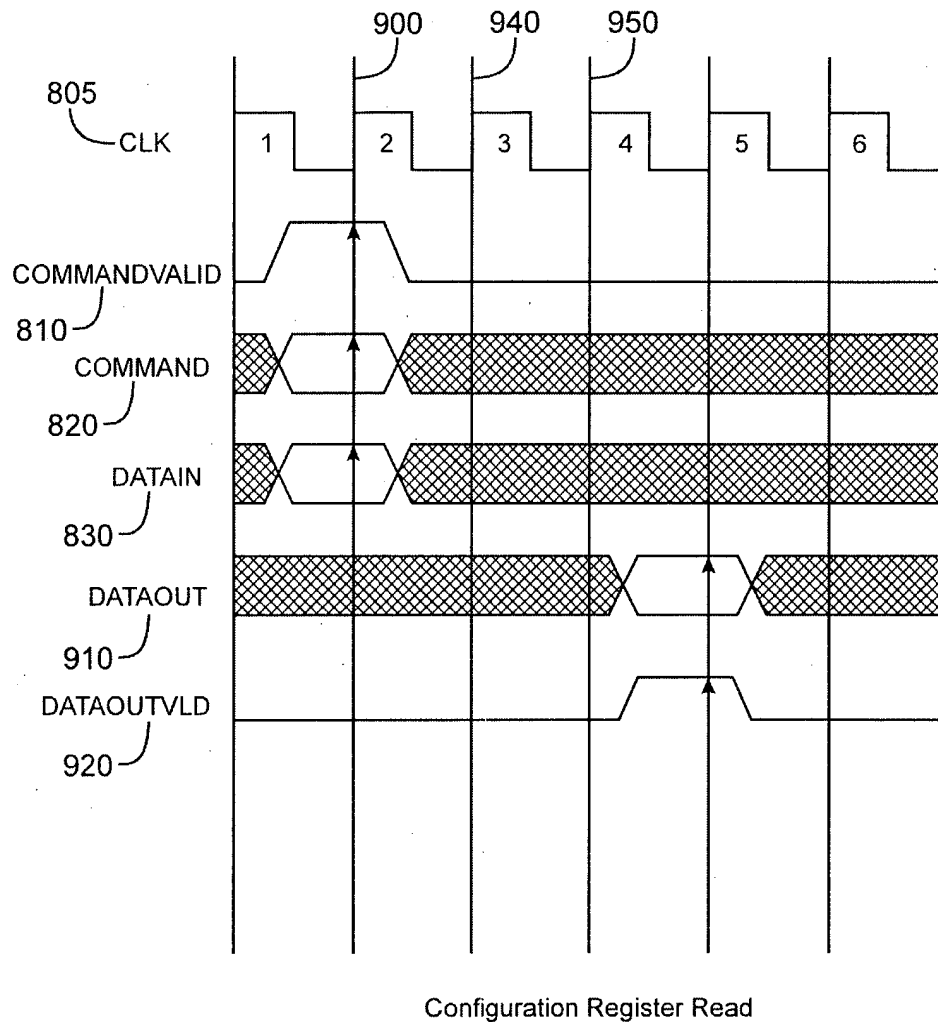


FIG. 9



9 / 10

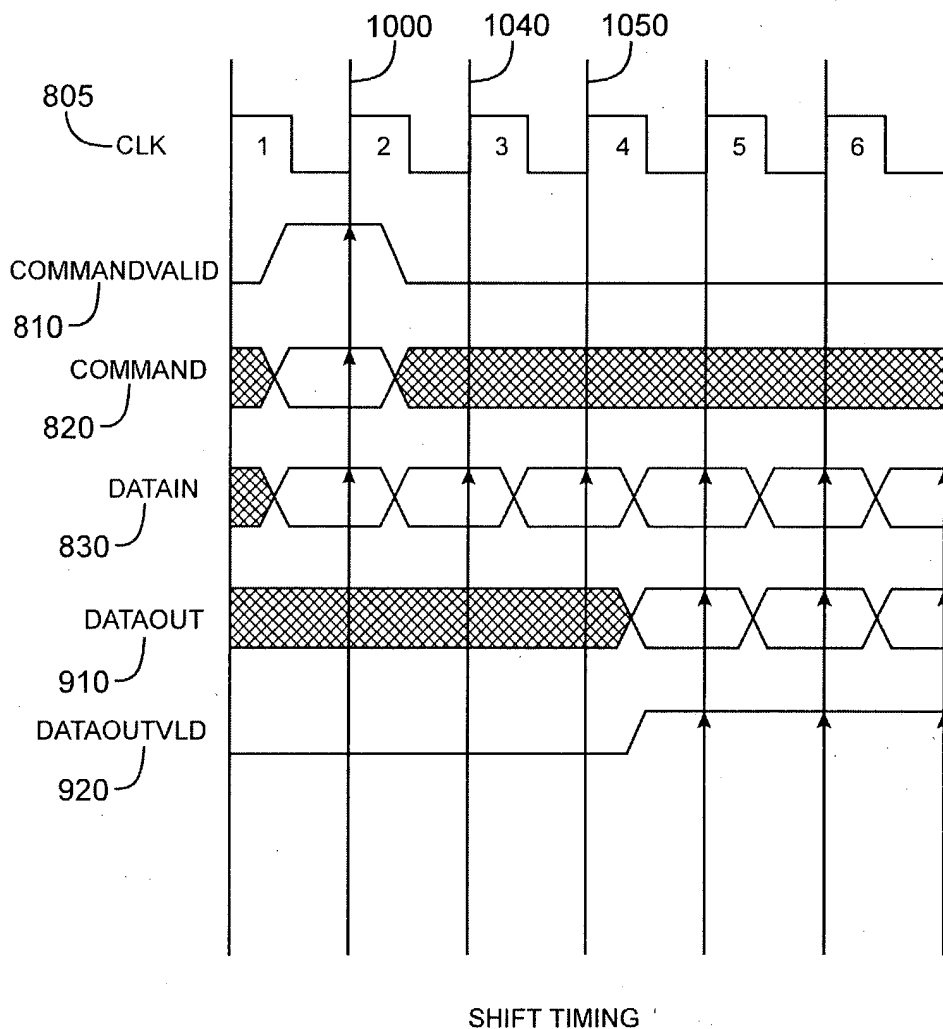


FIG. 10



10 / 10

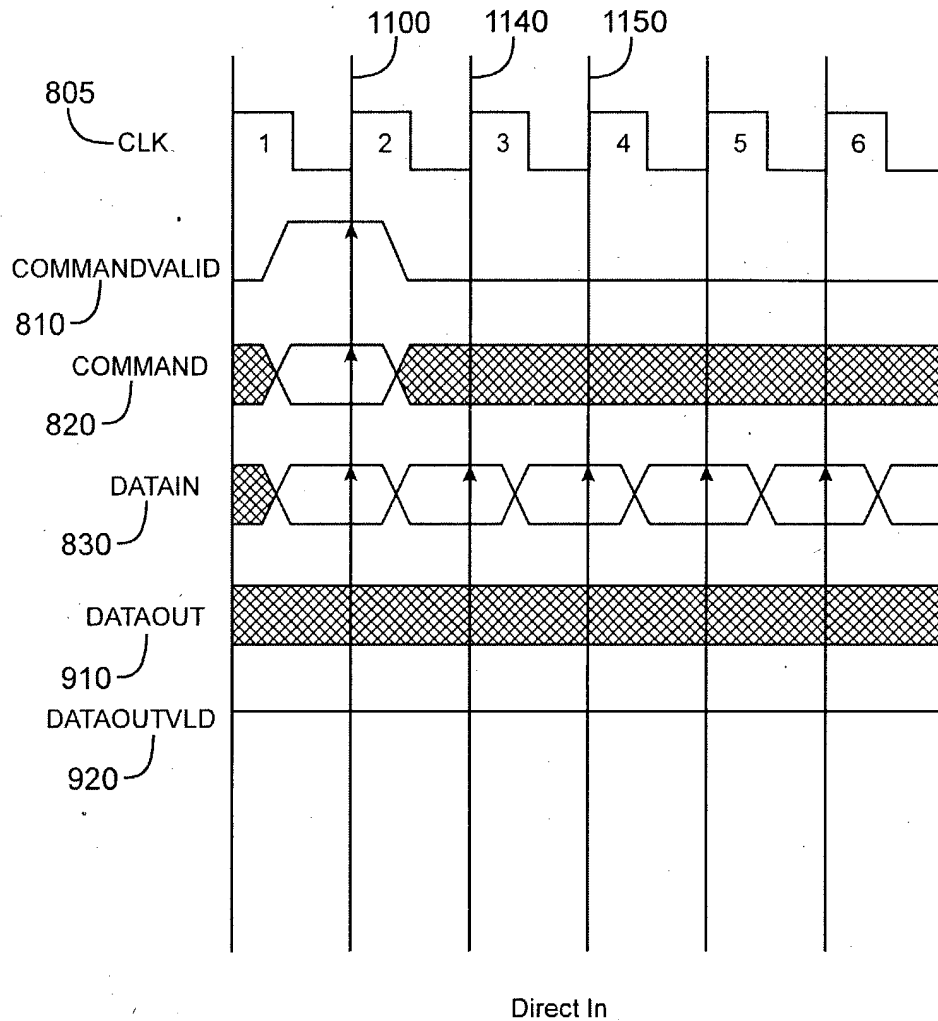


FIG. 11



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